

1 **51357/SDB/B600**

WHAT IS CLAIMED IS:

1. A first-in-first-out (FIFO) circuit comprising:
 - 5 a plurality of FIFO registers;
 - a write pointer circuit having an input that receives a write clock signal and a plurality of outputs that respectively couple to the plurality of registers, the write pointer circuit generates a write pointer signal at a first frequency; and
 - 10 a read pointer circuit having an input that receives a read clock signal and a plurality of outputs that respectively couple to the plurality of registers, the read pointer circuit generates a read pointer signal at a second frequency that is different than the first frequency.

20

25

30

35